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NOTICE OF ALLOWANCE AND FEE(S) DUE

65913 7590 11/19/2008 EX

NXP, B.V. NXP INTELLECTUAL PROPERTY DEPARTMENT M/S41-SJ 1109 MCKAY DRIVE SAN JOSE, CA 95131

| EXAMINER | | | | | | |
|----------|--------------|--|--|--|--|--|
| DINH, | MINH | | | | | |
| ART UNIT | PAPER NUMBER | | | | | |
| 2422 | | | | | | |

DATE MAILED: 11/19/2008

| APPLICATION NO. | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
|-----------------|-------------|----------------------|---------------------|------------------|
| 09/749.142 | 12/27/2000 | Thomas Wille | DE000002 | 4761 |

TITLE OF INVENTION: DATA-PROCESSING DEVICE AND METHOD OF OPERATING SAID DEVICE

| APPLN. TYPE | SMALL ENTITY | ISSUE FEE DUE | PUBLICATION FEE DUE | PREV. PAID ISSUE FEE | TOTAL FEE(S) DUE | DATE DUE |
|----------------|--------------|---------------|---------------------|----------------------|------------------|------------|
| nonprovisional | NO | \$1510 | \$0 | \$0 | \$1510 | 02/19/2009 |

THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT. PROSECUTION ON THE MERITS IS CLOSED. THIS NOTICE OF ALLOWANCE IS NOT A GRANT OF PATENT RIGHTS. THIS APPLICATION IS SUBJECT TO WITHDRAWAL FROM ISSUE AT THE INITIATIVE OF THE OFFICE OR UPON PETITION BY THE APPLICANT. SEE 37 CFR 1.313 AND MPEP 1308.

THE ISSUE FEE AND PUBLICATION FEE (IF REQUIRED) MUST BE PAID WITHIN THREE MONTHS FROM THE MAILING DATE OF THIS NOTICE OR THIS APPLICATION SHALL BE REGARDED AS ABANDONED. THIS STATUTORY PERIOD CANNOT BE EXTENDED. SEE 35 U.S.C. 151. THE ISSUE FEE DUE INDICATED ABOVE DOES NOT REFLECT A CREDIT FOR ANY PREVIOUSLY PAID ISSUE FEE IN THIS APPLICATION. IF AN ISSUE FEE HAS PREVIOUSLY BEEN PAID IN THIS APPLICATION (AS SHOWN ABOVE), THE RETURN OF PART B OF THIS FORM WILL BE CONSIDERED A REQUEST TO REAPPLY THE PREVIOUSLY PAID ISSUE FEE TOWARD THE ISSUE FEE NOW DUE.

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If the SMALL ENTITY is shown as YES, verify your current SMALL ENTITY status:

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If the SMALL ENTITY is shown as NO:

A. Pay TOTAL FEE(S) DUE shown above, or

B. If applicant claimed SMALL ENTITY status before, or is now claiming SMALL ENTITY status, check box 5a on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and 1/2 the ISSUE FEE shown above.

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III. All communications regarding this application must give the application number. Please direct all communications prior to issuance to Mail Stop ISSUE FEE unless advised to the contrary.

IMPORTANT REMINDER: Utility patents issuing on applications filed on or after Dec. 12, 1980 may require payment of maintenance fees. It is patentee's responsibility to ensure timely payment of maintenance fees when due.

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Complete and send this form, together with applicable fee(s), to: Mail Mail Stop ISSUE FEE

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| M/S41-SJ | 7590 11/19 CTUAL PROPERT | I I St ad tra | nereby certify that the description of the description of the Maintenance of the Maintena | nis Fee(with suf il Stop | e of Mailing or Transn s) Transmittal is being ficient postage for first ISSUE FEE address 1) 273-2885, on the da | deposited with the United t class mail in an envelope above, or being facsimile | |
| 1109 MCKAY DRIVE SAN JOSE, CA 95131 | | | | | | | (Depositor's name) |
| SAN JOSE, CA | 75151 | | | | | | (Signature) |
| | | | L | | | | (Date) |
| APPLICATION NO. | FILING DATE | | FIRST NAMED INVENTO | PR | ATTO | RNEY DOCKET NO. | CONFIRMATION NO. |
| 09/749,142 | 12/27/2000 | • | Thomas Wille | | | DE000002 | 4761 |
| APPLN. TYPE | SMALL ENTITY | ISSUE FEE DUE | PUBLICATION FEE DUI | E PREV. PAID ISSU | JE FEE | TOTAL FEE(S) DUE | DATE DUE |
| nonprovisional | NO | \$1510 | \$0 | \$0 | | \$1510 | 02/19/2009 |
| EXAM | | ART UNIT | CLASS-SUBCLASS | 7 | | 41010 | 02/13/2003 |
| DINH, | MINH | 2432 | 713-193000 | _ | J | | |
| "Fee Address" ind PTO/SB/47; Rev 03-0 Number is required. 3. ASSIGNEE NAME A | ND RESIDENCE DATA less an assignee is ident h in 37 CFR 3.11. Com | " Indication form ned. Use of a Customer A TO BE PRINTED ON | • | gle firm (having as a gent) and the nan torneys or agents. If e printed. ype) patent. If an assign assignment. | a memb nes of u no nam | er a 2p to lee is 3lentified below, the do | cument has been filed for |
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| 5. Change in Entity Sta | , | | | 1 | II ENE | PUTTY C | TD 1.07(\/0\ |
| NOTE: The Issue Fee an | as SMALL ENTITY state and Publication Fee (if requerecords of the United Sta | | b. Applicant is no lost from anyone other than Coffice. | | | | e assignee or other party in |
| | | | | Date | | | |
| Authorized Signature Typed or printed name | | | | | | | |
| This collection of inform | nation is required by 37 C tiality is governed by 35 d application form to the ions for reducing this bu /irginia 22313-1450. DC | ED 1 311 The informati | on is required to obtain o | r ratain a banafit by | the publ | lic which is to file (and | by the USPTO to process) g gathering, preparing, and he you require to complete rtment of Commerce, P.O. or Patents, P.O. Box 1450, |

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| NXP, B.V. | | DINH, MINH | | |
| | TUAL PROPERTY DE | ART UNIT | PAPER NUMBER | |
| M/S41-SJ 1109 MCKAY DF SAN JOSE, CA 9: | | 2432 DATE MAILED: 11/19/200 | 8 | |

Determination of Patent Term Adjustment under 35 U.S.C. 154 (b)

(application filed on or after May 29, 2000)

The Patent Term Adjustment to date is 294 day(s). If the issue fee is paid on the date that is three months after the mailing date of this notice and the patent issues on the Tuesday before the date that is 28 weeks (six and a half months) after the mailing date of this notice, the Patent Term Adjustment will be 294 day(s).

If a Continued Prosecution Application (CPA) was filed in the above-identified application, the filing date that determines Patent Term Adjustment is the filing date of the most recent CPA.

Applicant will be able to obtain more detailed information by accessing the Patent Application Information Retrieval (PAIR) WEB site (http://pair.uspto.gov).

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office of Patent Legal Administration at (571)-272-7702. Questions relating to issue and publication fee payments should be directed to the Customer Service Center of the Office of Patent Publication at 1-(888)-786-0101 (571)-272-4200.

| | Application No. | Applicant(s) | | | |
|--|---|---|--------------------------|--|--|
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| Notice of Allowability | 09/749,142 Examiner | WILLE ET AL. Art Unit | | | |
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| | MINH DINH | 2432 | | | |
| The MAILING DATE of this communication appear All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI of the Office or upon petition by the applicant. See 37 CFR 1.313 | (OR REMAINS) CLOSED or other appropriate comm GHTS . This application is | in this application. If not include nunication will be mailed in due o | d course. THIS | | |
| 1. 🔀 This communication is responsive to the after-final amende | ment filed 11/03/08 and the | Examiner's amendment author | <u>ized 11/10/08</u> . | | |
| 2. ☐ The allowed claim(s) is/are 2-4 and 7-14. | | | | | |
| 3. Acknowledgment is made of a claim for foreign priority ur a) All b) Some* c) None of the: 1. Certified copies of the priority documents have 2. Certified copies of the priority documents have | been received. | ,, | | | |
| 3. Copies of the certified copies of the priority do | • • | | ion from the | | |
| International Bureau (PCT Rule 17.2(a)). | | ou in the hadenal stage approach | | | |
| * Certified copies not received: | | | | | |
| Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE. | | le a reply complying with the req | uirements | | |
| 4. A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give | | | OTICE OF | | |
| 5. CORRECTED DRAWINGS (as "replacement sheets") mus | et be submitted. | | | | |
| (a) \square including changes required by the Notice of Draftspers | on's Patent Drawing Revie | ew (PTO-948) attached | | | |
| 1) hereto or 2) to Paper No./Mail Date | | | | | |
| (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date | | | | | |
| Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in t | | | back) of | | |
| 6. DEPOSIT OF and/or INFORMATION about the depo attached Examiner's comment regarding REQUIREMENT | | | lote the | | |
| | | | | | |
| Attachment(s) 1. ☐ Notice of References Cited (PTO-892) | 5. ☐ Notice of I | nformal Patent Application | | | |
| 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) | 6. 🔲 Interview S | Summary (PTO-413), | | | |
| 3. Information Disclosure Statements (PTO/SB/08), | Paper No 7. ⊠ Examiner' | ./Mail Date s Amendment/Comment | | | |
| Paper No./Mail Date 4. Examiner's Comment Regarding Requirement for Deposit of Biological Material | 8. 🛛 Examiner' | s Statement of Reasons for Allo | wance | | |
| or biological material | 9. 🔲 Other | <u>_</u> . | | | |
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EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Elliott D. Light on 11/10/2008.

The application has been amended as follows:

2. (Currently Amended) A method of operating a data-processing device with an integrated circuit comprising one two or more processors in which the integrated circuit performs cryptographic operations, the method comprising:

performing cryptographic operations in the integrated circuit, wherein at least two of the one or more processors perform the cryptographic operations simultaneously and in parallel,

wherein the cryptographic operations of at least one of the one or more processors

processor are useful operations and the cryptographic operations performed by at least one other processor are dummy operations whose results are rejected, and wherein the selection as to which of the one two or more processors performs a useful operation is randomly controlled, and

wherein consumption characteristics of the data-processing device being a superimposition of consumption characteristics associated with performing both useful and rejected cryptographic operations, whereby reconstruction of the consumption characteristics associated with performing any of the useful cryptographic operations is impeded.

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3. (Currently Amended) A method as claimed in claim 2, wherein the one two or more processors comprise a CPU and at least one co-processor.

10. (Currently Amended) A data-processing device with an integrated circuit, comprising:

one two or more processors,

a control unit which controls the one two or more processors so that, in the case of a cryptographic operation, the one or more processors perform at least one processor performs a cryptographic operation simultaneously and in parallel with the performance of a with at least one dummy operation by at least one other processor, and wherein the selection as to which of the one two or more processors performs a useful operation is randomly controlled,

whereby consumption characteristics associated with performing the respective cryptographic and dummy operations are superimposed so that reconstruction of the consumption characteristics associated with performing the cryptographic operation is impeded.

- 13. (Currently Amended) A data-processing device as claimed in claim 12, wherein the recombiner rejects the relevant result of the at least one processor of the one or more processors that performed such dummy operation.
- 14. (Currently Amended) A data-processing device as claimed in claim 10, wherein the one two or more processors comprise a CPU and at least one co-processor.

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The following is an examiner's statement of reasons for allowance. The present invention is directed to a method for performing cryptographic operations using two or more processors in an integrated circuit wherein at least two processors perform the cryptographic operations simultaneously and in parallel, wherein the cryptographic operations of at least one processor are useful operations and the cryptographic operations performed by at least one other processor are dummy operations whose results are rejected. More specifically, independent claims 2 and 10 identify the uniquely distinct features: wherein the selection as to which of the two or more processors performs a useful operation is randomly controlled. The closest prior art includes: (i) Patarin et al. (6,658,569) discloses two or more processors performing useful cryptographic operations simultaneously and in parallel; and (ii) Jahnich et al. (6,725,374) discloses using dummy operations which cause additional advantageous current fluctuation to be observed in a PDA analysis. The prior art, taken either singly or in combination, fails to anticipate or fairly suggest the limitations of applicant's independent claim, in such a manner that a rejection under 35 U.S.C 102 or 103 would be proper. The claimed invention is therefore considered to be in condition for allowance as being novel and nonobvious over prior art.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to MINH DINH whose telephone number is (571)272-3802. The examiner can normally be reached on Mon-Fri: 10:00am-6:30pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Gilberto Barron can be reached on 571-272-3799. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/M. D./ Examiner, Art Unit 2432

11/10/08

/Gilberto Barron Jr/ Supervisory Patent Examiner, Art Unit 2432